# Beyond Decoding: Meta-Generation Algorithms for Large Language Models

Presenters: Matthew Finlayson, Hailey Schoelkopf, Sean Welleck December 11, 2024

Efficient meta-generation

# Efficiency | goals

### Scope:

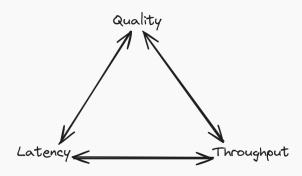
- · Basics of efficient generation
- · How can we make meta-generation faster?
- · Which specific meta-generators are most efficient?

# Efficiency | basics

How do we measure "efficiency"?

- · Latency
  - · How long does a user wait for a response?
- · Throughput
  - · How many requests are completed per second?

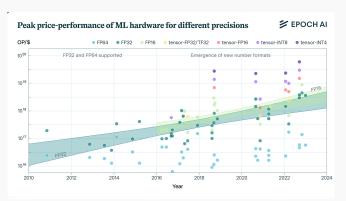
# Efficiency | basics



Latency, Throughput, and Quality often trade off at a given budget.

# Efficiency | hardware

Hardware improvements have driven model improvements <sup>21</sup>



The largest efficiency wins come from mapping operations onto hardware (more) effectively!

<sup>&</sup>lt;sup>21</sup>Figure: [Hobbhahn et al., 2023]

# Efficiency | hardware

How do ML accelerator designs impact generation efficiency?

- · How much data can we keep on-device?
  - · VRAM (GB)
- · How many operations/second can the device perform?
  - · FLOP/s
- How long does it take to send operands from GPU memory (HBM) to the processor?
  - · Memory Bandwidth (GB/s)

# Efficiency | bottlenecks

- · Loading inputs (activations) from memory
  - · Memory Bandwidth
- · Loading weights from memory
  - · Memory Bandwidth
- Performing computation
  - · FLOP/s
- · Communicating across devices
  - · Communication Speeds (GB/s)

• ...

# Efficiency | bottlenecks

Time per operation can be modeled as<sup>22</sup>:

$$\mathrm{Time} = \mathrm{max}\left(\frac{\mathrm{Operation\ FLOP}}{\mathrm{Device\ FLOP/s}}, \frac{\mathrm{Data\ Transferred\ (GB)}}{\mathrm{Memory\ Bandwidth\ (GB/s)}}\right)$$

Operations are either "compute-bound" or "memory-bound" 23

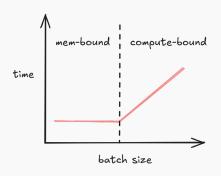
<sup>&</sup>lt;sup>22</sup>[He, 2022]

 $<sup>^{23}</sup>$  H100 SXM: BF16 dense tensor core max FLOP/s  $\approx 1\times 10^{15}$  FLOP/s, Memory bandwidth  $\approx 3.35\times 10^{12}$  byte/s.  $\gg 100$  FLOP/byte is "free"!

# Efficiency | batching



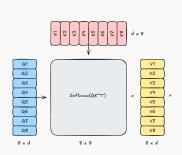
Inputs to a model can be "batched" together and computed simultaneously.



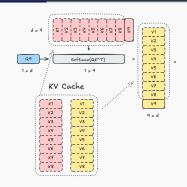
Batching can be cost-free for memory-bound operations!<sup>a</sup>

 $<sup>^</sup>a {\it https://www.artfintel.com/p/how-does-batching-work-on-modern}$ 

# Efficiency | KV cache



**Prefill Stage**: process prompt all at once. Keys and values retained and initialize the "KV Cache".



**Decode Stage**: use cached KV values to compute attention for current timestep. Append new K, V to KV cache

$$Size = (batch \cdot n\_ctx) \cdot (2 \cdot n\_layer \cdot n\_heads \cdot head\_dim) \cdot (n\_bytes)$$

Efficient meta-generation

token?

How to speed up sampling a single

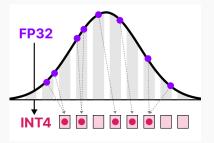
For a single decoding step, how do we work around hardware constraints?

- · Memory Bandwidth  $\downarrow$
- FLOP/s ↑
- FLOP ↓

#### Memory Bandwidth ↓: reduce data transferred

Quantize weights or activations<sup>24</sup>

(bytes per parameter)  $\cdot$  (total parameters)



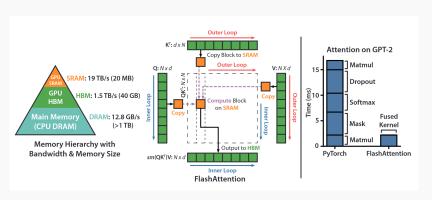
· Compress or distill model

(bytes per parameter) · (total parameters)

<sup>&</sup>lt;sup>24</sup>Visual from https://newsletter.maartengrootendorst.com/p/a-visual-guide-to-quantization

**FLOP/s** ↑: improve hardware utilization

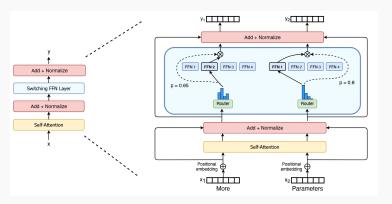
(FLOP per second) · (total operation FLOP)



Flash Attention [Dao et al., 2022] performs the same operations, but optimizes the implementation to achieve far greater speed

**FLOP** ↓: reduce operations required

#### (FLOP per second) · (total operation FLOP)



Mixture-of-Experts models use fewer FLOP per token than equi-parameter dense models [Fedus et al., 2022]

# Efficient meta-generation

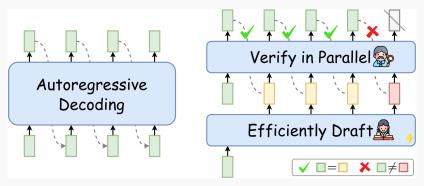
How to speed up a single generation?

Generation of long outputs is bottlenecked by sequential next-token prediction. But not all tokens are created equal!

... The cow jumped over the moon . <EOS>

How can we spend less time on "easier" tokens?

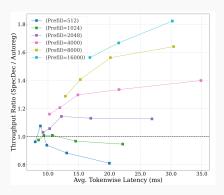
Decoding is typically **memory-bound**.



Speculative decoding uses a smaller **draft** model to produce "guesses" for the next N tokens cheaply, which are then "accepted" or "rejected" in parallel by the main model [Xia et al., 2024]

#### In speculative decoding:

- · A lighter-weight draft model generates N "proposal" tokens
- These N "proposal" tokens can be passed in parallel into the main generator
- All tokens which match the main generator's predictions are retained, and ones that do not are discarded



Speculative decoding can harm throughput at low context but improves both throughput and latency at long context lengths [Chen et al., 2024a]

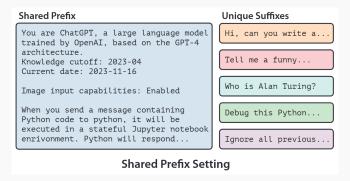
# Efficient meta-generation

How to speed up meta-generation?

# Efficiency | meta-generators

- How do meta-generators interact with real-world efficiency and hardware utilization?
- Which meta-generators are the fastest? Can we design more efficient meta-generators?

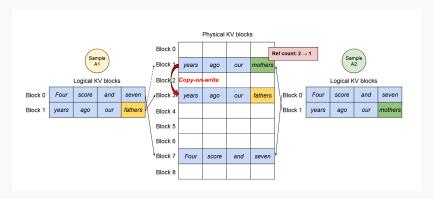
## Efficiency | meta-generators | KV Cache reuse



Common deployment and parallel generation scenarios have redundant shared prefix content in prompts<sup>25</sup>

<sup>&</sup>lt;sup>25</sup>Figure from [Juravsky et al., 2024]

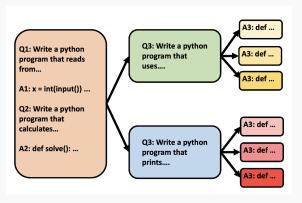
# Efficiency | meta-generators | KV Cache reuse



PagedAttention [Kwon et al., 2023] prevents redundant storage costs by mapping KV cache blocks to physical "pages" of VRAM

# Efficiency | meta-generators | KV cache reuse

KV Cache reuse is not limited to single-level shared prefixes!



Multiple levels of prefix sharing can arise frequently: for example, combining a long few-shot prompt with Best-of-N generation<sup>26</sup>

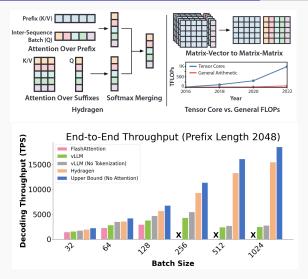
<sup>&</sup>lt;sup>26</sup>Figure from [Juravsky et al., 2024]

# Efficiency | meta-generators | KV Cache reuse



RadixAttention enables complex prefix sharing patterns [Zheng et al., 2024], evicting least-recently-used KV cache blocks from memory when needed

# Efficiency | meta-generators | KV Cache reuse



Hydragen [Juravsky et al., 2024] makes shared-prefix attention components faster via leveraging Tensor Cores

KV Cache size is a key bottleneck to larger batches and to longer context inference

- Token Dropping: Selectively remove tokens from the KV Cache
- · Quantization: Modify KV Cache datatype
- Architectural Modification: Reduce inherent size of a prospective model's KV Cache

### **Token Dropping:**

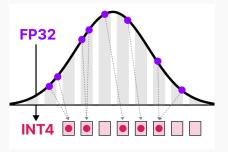
$$(\text{batch} \cdot \underline{\mathbf{n}}_{\text{ctx}}) \cdot (2 \cdot \underline{\mathbf{n}}_{\text{layer}} \cdot \underline{\mathbf{n}}_{\text{heads}} \cdot \text{head}_{\text{dim}}) \cdot (\underline{\mathbf{n}}_{\text{bytes}})$$



An overview of approaches to control KV Cache size via *token dropping* [Adams et al., 2024]

#### Quantization:

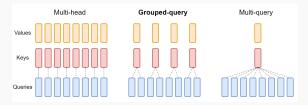
$$(\text{batch} \cdot \text{n\_ctx}) \cdot (2 \cdot \text{n\_layer} \cdot \text{n\_heads} \cdot \text{head\_dim}) \cdot (\text{n\_bytes})$$



As with model weights, elements of the KV cache can be *quantized* to reduce memory overheads

#### Architectural Modification:

$$(\text{batch} \cdot \text{n\_ctx}) \cdot (2 \cdot \text{n\_layer} \cdot \text{n\_heads} \cdot \text{head\_dim}) \cdot (\text{n\_bytes})$$



Architectural tweaks such as Multi-Query Attention [Shazeer, 2019] or Grouped-Query Attention [Ainslie et al., 2023] reduce the number of Key + Value attention heads to shrink the required KV Cache size

# Efficiency | meta-generators | recap

Which meta-generators are most efficient?

- Parallelizable: trajectories can be run in parallel; not sequentially bottlenecked
- Prefix-shareable: long inputs are presented as identical shared prefix content, whose KV Caches can be reused across many model calls

**Token budget** is not the only indicator of meta-generator efficiency!

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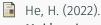
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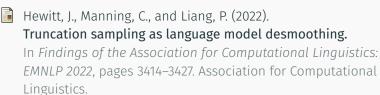
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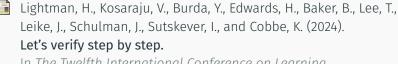
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